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VLSI Design Lecture 2: Basic Fabrication Steps And ... VLSI Design Lecture 2: Basic Fabrication Steps And Layout and Layout Shaahin Shaahin Hessabi Hessabi Department Of Computer Engineering Sharif University Of Technology Adapted With Modifications From Lecture Notes Prepared By The Book Author The Book Author (from Prentice Hall PTR) (from Prentice Hall PTR) Apr 4th, 2024 VLSI FABRICATION TECHNOLOGY VLSI FABRICATION TECHNOLOGY Introduction Since The First Edition Of This Text, We Have Witnessed A Fantastic Evolution In VLSI (very-large-scale integrated circuits) technology. In the late 1970s, non-self-aligned metal gate MOSFETs with gate lengths in the order of  $1 \mu\text{m}$  were used. Introduction To VLSI Fabrication Technologies CMOS Process The CMOS process allows fabrication of NMOS and PMOS transistors side-by-side on the same silicon substrate. 12 Organization Materials Used In VLSI Fabrication VLSI Fabrication Technologies Overview Of Fabrication Methods Device Simulation. 13 Fabrication Process Sequence Mar 2th, 2024.

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