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Reset EXTERNAL ADDRESS BUS EXTERNAL DATA BUS  
Figure 4: Block Diagram For The A Apr 3th,  
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DesignOverview Of Power Consumption • The Average  
Power Consumption Can Be Expressed As  $1 \text{ Avg C Load V DD C Load V DD F CLK T P 2}$  • The Node Transition  
Rate Can Be Slower Than The Clock Rate. To Better  
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Textbook: B. Lathi, Signal Processing & Linear Systems. Berkeley-Cambridge, 1998.  
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Downsampling Taking One Sample Every M Samples Of A Given Sequence Is An Operation Called  
Decimation Of A Factor M. In Practice It Reduces The Sampling Frequency Of A Factor M (downsampling).  
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