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Chapter 8 Memory Hierarchy And Cache Memory• Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory • T Cache = 1 Cycle, T MM = 100 Cycles • What Is The AMAT Of The Program From Example 1? AMAT = T Cache + MR Cache (t MM) = [1 + 0.375(100)] Cycles = 38.5 Cycles Memory Performance Example 2 Feb 7th, 20247-1 Chapter 7- Memory System Design Chapter 7- Memory ...7-2 Chapter 7- Memory System Design Computer Systems Design And Architecture By

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