

# Transistor Database User Guide

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CMOS Families In Section 3.8. We'll Use The Following

Definitions Of LOW And HIGH In Our Discussions Of TTL

Circuit Behavior: LOW 0-0.8 Volts. HIGH 2.0-5.0 Volts.

TTL.1 Basic TTL NAND Gate The Circuit Diagram For A

2-input LS-TTL NAND Gate, Part Number 74LS00, Is

Shown In Figure Jan 3th, 2024

**1954 Germanium Transistor Data**

**Chart. Transistor Products ...**

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Products-Radio Receptor-Hydro Air-National Union-

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Raytheon-Sylvania Rufus P. Turner Author-Package  
Outline Diagrams-www.33audio.com. May 7th, 2024

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Cable Information 2 Cable Cable Type DALI 2-wire  
Mains-rated, 0.5 Mm<sup>2</sup> To 1.5 Mm<sup>2</sup> Max. Length: 300 M  
(with 1.5 Mm<sup>2</sup> Cable). Example: Belden 8471. Mains  
Cable / Dimmed Outputs Max. 2.5 Mm<sup>2</sup> Stranded (4  
Mm<sup>2</sup> Solid). SDIM / DMX L Jan 4th, 2024

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Howard W. Sams & Co 2000-10-01 This Completely  
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Mar 01, 2005 · Transistor That We Use In The Lab  
(Figure 1(b)) Is A Combination Of Two Transistors,  
Which Form A Configuration Known As A Darlington-  
pair. Figure 1(b) Shows How The Emitter Voltage From  
The First Transistor Is The Base Voltage Of The Second  
Transistor, Making The Darlington-pair Jan 4th, 2024

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