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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2]
3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [2th, 2024

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Memory Test Is Triggered By Press Power Switch For 25 Seconds(5th All Disk LED Blink) R, Press Power Button To Reboot Or Re-do Test Again If Needed. Memory Test Failed, Re-seat Memory Or Replace Memory. Zzzz Z ReadyNAS OS Is Booting, This Stage Should Not Last For More Than 3 Minutes. Otherwise, One Of Disk May Be Bad Or Incompatible. 2th, 2024

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