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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Feb 15th, 2024

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Test 2 Result Generalizations & Highlights Don't
Display = Delete Except Adobe Audition Within Chunks
(less Exceptions) Entire Chunks (more Exceptions) A

Couple Of Apps Appended Existing Data Automatically In The Bext And IXML Chunks Differing Behaviors Between Audio Edits Vs. Metadata Edits Jan 1th, 2024

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LITERATURE • [1] The Free Lunch Is Over: A Fundamental Turn Toward Concurrency In Software, Dr.Dobb's Journal, 30(3), Mar 13th, 2024

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