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Usually, We Call It A Chip Design. Anyone Who Is

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Chapter 4 Low-Power VLSI Design Power VLSI

Design Overview Of Power Consumption • The Average

Power Consumption Can Be Expressed As  $1 \text{ Avg C Load } V_{DD} C_{Load} V_{DD} F_{CLK} T P 2$  • The Node Transition

Rate Can Be Slower Than The Clock Rate. To Better

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