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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ Jan 22th, 2024

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6. AUTHOR(S) Avionics Department 5d. PROJECT NUMBER N/A 5e. TASK NUMBER N/A 5f. WORK UNIT NUMBER N/A 7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) Naval Air Warfare Center Weapons Division 575 I Avenue, Suite 1 (Code 450000E) Point Mugu, California 93042 8. PERFORMING ORGANIZATION REPORT NUMBER NAWCWD TP 8347 9. Feb 12th, 2024

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2. REPORT TYPE Final 3. DATES COVERED - 4. TITLE AND SUBTITLE Electronic Warfare And Radar Systems Engineering Handbook 5a. CONTRACT NUMBER 5b. GRANT NUMBER 5c. PROGRAM ELEMENT NUMBER 6. AUTHOR(S) NAVAIR Electronic Warfare/Combat Systems 5d. PROJECT NUMBER 5e. TASK NUMBER 5f. WORK UNIT NUMBER 7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) Mar 8th, 2024

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