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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE)

Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ Mar 1th, 2024

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# We Are Grateful To Per Johansson, Erik Mellander, Harald Niklasson And Seminar Participants At IFAU And IUI For Helpful Comments. Financial Support From The Institute Of Labour Market Pol-icy Evaluation (IFAU) And Marianne And Marcus Wallenbergs Stiftelse Is Gratefully Acknowl-edged. \* Corresponding Author. IUI, Box 5501, SE-114 85 ... Jun 1th, 2024

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