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### **8086 16-BIT HMOS MICROPROCESSOR 8086 8086-2 8086-1**

Will Release The Local Bus During T4 Of The Cycle When All The Following Conditions Are Met: 1.Request Occurs On Or Before T2. 2.Current Cycle Is Not The Low Byte Of A Word (on An Odd Address). 3.Current Cycle Is Not The First Acknowledge Of An Interrupt Acknowledge Sequence. 4.A Locked Instruction Is Not Currently Executing. 17th, 2024

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Ajay V Deshmukh Tmgh 2005 5 The 8085  
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Edition, Prentice Hall Of India, 2007. 2. Mohamed Ali  
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7th, 2024

### **8086 Microprocessor (cont..)**

8086 Microprocessor (cont..) M. Krishna Kumar  
MM/M1/LU3/V1/2004 2 • It Requires Single Phase Clock  
With 33% Duty Cycle To Provide Internal Timing. •  
8086 Is Designed To Operate In Two Modes, Minimum

And Maximum. • | 19th, 2024

## **UNIT III 8086 MICROPROCESSOR INTERFACING**

### **3.1 ...**

8086 Microprocessor Is Interfaced With Memory And Peripherals And How An 8086 Based Microcomputer System Works. 3.2 Learning Objectives To Study About The Operating Modes Of 8086 To Study About The Components Of An 8086 Based Microcomputer System To 9th, 2024

### **Microprocessor 8086 By B Ram - Flexreviews.tempurpedic.com**

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