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R. Delgado Carrascon Et Al., "Optimization Of GaN Nanowires Reformation Process By MOCVD For Device-Quality GaN Templates", Physica Status Solidi B 257, 1900581 (2020) D.-Y. Chen Et Al., "Microwave Performance Of 'Buffer-Free' GaN-on-SiC High Electron Mobility Transistors," IEEE Electron Dev. Lett. 41, 828 (2020) Mar 21th, 2024

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1 III-V Ferromagnetic Semiconductors F. Matsukuraa,b), H. Ohnoa), And T. Dietla,b) A) Laboratory For Electronic Intelligent Systems, Research Institute Of Electrical Communication, Tohoku University, Sendai, Japan B) Institute Of Physics, Polish Academy Of Sciences, Warszawa, Poland CONTNETS 1. Introduc Apr 11th, 2024

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Cleanroom Jack Whaley, Bob Hill And Mike Anzlowar, All Of You Have My Unending Gratitude. For Administrative Support And So Much More Æ Thank You Cathy Fogarty, Lee Baboolal, Joann Erving And Val De Veyra. And Thanks To The Guys In The Electrical Engineering Workshop - May 1th, 2024

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