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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Feb

3th, 2024

LOW FREQUENCY SMD CRYSTALLLOW FREQUENCY SMD ...

LOW FREQUENCY SMD CRYSTALLLOW FREQUENCY SMD CRYSTALOW FREQUENCY SMD CRYSTAL STANDARD SPECIFICATIONS: OUTLINE DRAWING: MARKING INSTRUCTIONS: OPTIONS AND PART IDENTIFICATION (Left Blank If Standard): 8.0 X 3.8 X 2.5mm TABLE 1: 20.000 To 27.000 (Fund) 30 9.000 To 12.999 (Fund) 60 13.000 To 15.999 ... Mar 3th, 2024

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GHz VCC = 3.0V To 5.5V 0.1 Jan 1th, 2024

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