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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE)
Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2]
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Gang: Ekstern Betjenings-knapp Tilkobling: Maks 20V DC Maks 25mA Ved Aktiv Enhet Maks 0,1mA I Hvile. Rød Lysdiode 1-pol No Til Systemsp. Plus Maks 20V DC Maks 20mA 2 Pol 6,35mm Jack ... 1 Sett Består Av: 1 Stk Minne-enhet 1 S May 1th, 2024

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