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Lecture 14 Field Effect Transistor (FET)
Metal-Oxide-Semiconductor Field-Effect Transistor (MOSFET)
JUNCTION FIELD EFFECT TRANSISTOR (JFET) FET 1-5 .
Construction Of JFET FET 1-6 . Characteristics Of JFET
By Applying Voltage At The JFET's Terminal ($V_{GS} = 0$
 V And $V_{DS} = +ve$ Values), Some Characteristics Can
Be Obtained For The N-type Material, Electrons Will Be
Attracted To The Positive Terminal Of V_{DS} For The P-
type Material At ... May 3th, 2024
PMV213SN
UTrenchMOS(tm) Standard Level FET
Pder 03aa25 0 40
80 120 0 50 100 150 200 Tsp (°C) Ider (%) Pder Ptot P

... Package Outline Fig 14. SOT23. UNIT A1 Max. B
PcDE E1 HE L Qwv OUTLINE REFERENCES VERSION
EUROPEAN PROJECTION ISSUE DATE 97-02-28
99-09-13 IEC JEDEC EIAJ Mm 0.1 0.48 0.38 0.15 0.09
3.0 2.8 1.4 1.2 Apr 17th, 2024Si9410DY N-channel
TrenchMOS™ Logic Level FETPder (%) 03aa19 0 40 80
120 0 50 100 150 200 (C) Ider (%) Pder Ptot P ...
Package Outline Fig 14. SOT96-1 (SO8). UNIT A Max.
A1 A2 A3 Bp CD (1) E(2) EH (1) E LLp QZv W Y ... Jan
14th, 2024.
PHD16N03LT N-channel TrenchMOS™ Logic Level
FETPder (%) 03aa24 0 40 80 120 0 50 100 150 200
Tmb °C) Ider (%) Pder Ptot P ... Plastic Single-ended
Surface Mounted Package (Philips Version Of D-PAK); 3
Leads (one Lead Cropped) SOT428 E B2 E1 B1 BcwAM
L L1 13 2 D D1 HE L2 Note 1. Measured From Heatsink
Back To Lead. E1 E A A2 A A Y Seating Plane Mounting
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