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Wiley, 1986. [9] Macromodeling With SPICE, J.A. Conelly, P. Choi, Prentice Hall, Englewood Cliffs, New Jersey, 1997 [10] Selected Copies Of Journal Papers And Notes. Objective: To Understand, Design, And Test IC Analog Components, And Building Blocks In CMOS Technology. To Grasp The Relationships Between Devices, Circuits And Systems. Feb 16th, 2024

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