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Key Words: Digital Circuit, VHDL, Entity, Architecture. INTRODUCTION VHDL Means VHSIC Hardware Description Language Where VHSIC Stands For Very-High-Speed Integrated Circuit. VHDL Is A Hardware Description Language That Can Be Used To Model A Digital System .VHDL Is Commonly Used As A Design-entry Language For Field-programmable Gate Arrays 5th, 2024

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Constructed Digital Circuit, VHDL Design Will Be Continued To Carry Out The System Implementation. Figure 4. Tested Circuit Of Simple Stop Watch Circuit Using Two Displays Now, It Was Found To Be Explained That Is Why Needed To Use FPGA Based System On Chip Design. Following Section Will Be Presented VHDL Design With More Functions, More 2th, 2024

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Hardware Description Languages VHDL = VHSIC Hardware Description Language (VHSIC = Very High Speed Integrated Circuits) Developed By DOD From 1983 -based On ADA Language IEEE Standard 1076-1987/1993/2002/2008 Gate Level Through System Level Design And Verification Verilog -created In 1984 By Phil Moorby And Prabh 1th, 2024

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