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**Digital PID Controller Design Digital PID Controller Design**

Digital PID Controller Design <sup>2</sup> Let  $T_1, \dots, T_K$  Denote The Real Distinct Zeros Of  $T(u; \frac{1}{2})$  of odd Multiplicity, For  $U \geq 2$  ( $i=1;1$ ), Ordered As Follows:  $i_1 < T_1$