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Electronics And Communication Engineering
Department2nd Year 2nd Semester: A. Theory Sl. No.
Course Code Course Name Contact Hours/Week Credit
L T P Total Points 1 CHEM2001 Basic Environmental
Engineering And Ecology 3 0 0 3 3 2 ECEN2201 EM
Theory & Transmission Line 3 0 0 3 3 ECEN2002
Digital Electronics 3 0 0 3 3 4 ECEN2203 Analog
Communication 3 0 0 3 3 5 ECEN2204 Solid State
Devices 3 0 0 3 3 Mar 5th, 2024Electronics &
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Space & No. Students Can Perform Experiment Quality
Of Instruments Basic Electronics Lab 1250 Sqm, 40 All

The Basic Equipment Including CRO, Function Generator, Multi Utility Power Supply Unit, Trainer Kit, Modern Digital Multimiter, Digital Electronics Lab 1250 Sqm, 40 All The Basic Equipment Including CRO, Function Generator, Jan 3th, 2024Department Of Electronics And Communication Engineering4. G.F.Sudha And B.Nanchariah, "A Distributed Routing Algorithm For Data Gram Traffic In Teledesic System", International Conference On Wireless Communication Networks, SSN College Of Engineering, Chennai, 27-28 June 2003. 5. G.F.Sudha And T.G.Palanivelu, "Visibility Enhancement Of Subsurface Tissue Images", Apr 6th, 2024.

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Asso. Prof Easari. Parushuramu Asst. Proff Under Prof. K. Ashok Babu HOD Of ECE Dept. SriIndu College Of Engineering And Technology (an Autonomous Institution Under JNTUH) S May 2th, 2024. DEPARTMENT OF ELECTRONICS & COMMUNICATION ...Athanasios Papoulis And S. Unnikrishna Pillai, "Probability, Random Variables And Stochastic Processes", PHI, 4th Edition, 2002. References: ... Axiom 2: P(S) =1. Which Means That The Probability Of A Sample Space Consisting Of Al Jan 1th, 2024Electronics & Communication Engineering Department Batch ...10300314012 AMANDEEP ANAND 10300314013 AMIT KUMAR SINGH 10300314014 ANINDITA GOLDER 10300314015 ANKIT KUMAR ... 10300314067 MD JAWED 10300314068 MD KHALILUR RAHMAN 10300314069 MD NOORULLAH RAZA ... 10300314083 RAHUL KUMAR 10300314084 RAHUL MUKHERIEE . Electronics & Communication Engineering Department Apr 4th, 2024DEPARTMENT OF ELECTRONICS AND COMMUNICATIONFull Adder Using (i) Basic Logic Gates And (ii) NAND Gates. (b) Full Subtractor Using (i) Basic Logic Gates And (ii) NANAD Gates. 3 Design And Implement 4-bit Parallel Adder/ Subtractor Using IC 7483. 4 Design And Implementation Of 5-bit Magnitude Comparator Using IC 7485. Realize 5 (a) Adder ... May 4th, 2024. DEPARTMENT OF ELECTRONICS AND COMMUNICATION ...FULL ADDER: A Full Adder Is A Combinational Circuit That Focuses The Arithmetic Sum Of Three Bits. It

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