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SM 3 EECE 488 - Set 1: Introduction And Background References • Main Reference: Lecture Notes • Recommended Textbook: Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw- Hill, 2001 • Some Other Useful References: 6th, 2024

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EECE488 Analog CMOS Integrated Circuit Design Assignment 4 Due: Tuesday November 26th, 2013 At 8:00am 1. Consider The Following Circuit. (This Circuit Is Sometimes Referred To As A Self-biased Current Source).  $R = V_{DD} / GND$   $M_0$   $M_2$   $M_1$   $M_3$   $GND$   $4$   $GND$  | Out Assume All Transistors Are Operating In Saturation Region And  $I_{D0} = 0$ ,  $V_{DD} = 3$  V,  $V_{TH}(NMOS) = 0$  ... 8th, 2024

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