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Interleaved Power Factor Correction (IPFC) Using The DsPIC DSC

This Application Note Focuses On The Design Of An Interleaved Power Factor Correction (IPFC) Converter. It Explains The Digital Implementation Of The IPFC On A 16-bit Fixed Point DsPIC DSC, Containing The Theoretical Aspects Of Functioning, And MATLAB® Modeling. This Application Note Also Provides Hardware Design Mar 8th, 2024

DsPIC IAR C/EC++ Compiler

When You First Begin Using DsPIC IAR C/EC++ Compiler, You Should Read Part 1: Using The Compiler In This Reference Guide. When You Are Thoroughly

Familiar With The DsPIC IAR C/EC++ Compiler And Have Already Configured Your Project, You Can F Feb 7th, 2024

DsPIC Symmetric Key Embedded Encryption Library

NIST SP 800-38A CBC Wrapper(1) 903 NIST SP 800-38A CTR Wrapper(1) 348 AES (128-bit) FIPS 197 Basic Encryption 2505 232(3) FIPS 197 Basic Decryption 2895 FIPS 197 ECB Wrapper(1) 234 FIPS 113 CBC-MAC Encryption Wrapper(1) 663 NIST SP 800-38A CBC Decryption Wrapper(1) 357 NIST SP 800-38A CTR May 2th, 2024

Product Number: SW006013 MPLAB C Compiler For DsPIC DSCs

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DsPIC Language Tools Getting Started

The Simple Source Code In This Tutorial Is Designed For An MPLAB IDE V6.XX Project Which Will Be Created Next. It Will Use T He MPLAB SIM30 Simulator And The PIC30F6014 Device. The Tutorial Assumes That The

Directory C:\pic30_tools Is The Apr 3th, 2024

Data Encryption Routines For PIC24 And DsPIC Device

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DsPIC Digital Signal Controllers

System For Microchip's 16- And 32-bit Microcontrollers. Add MPLAB ICD 3 Or MPLAB REAL ICE™ In-circuit Debugger/ Programmer For Software Development. Microstick II (DM330013-2) Microstick II Delivers A Complete Development Hardware Platform For Microchip's 16-bit An Mar 5th, 2024

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