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And Outputs Works As Follows: $V_{in}(+)$ Is The Analog
Input, And $D0$ Is The Serial Output. Feb 3th, 2024.
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sigma PMCThe Input Module Is Designed With 256x
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Filtering Ensures That SNRs Of 86dB Are Readily
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Over-sampling Clock Via The Front Connector, And An
Internal Sample Clock, Which Is User Selectable. The
Internal Sampling Clock Utilizes A Frequency Jan 9th,
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