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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Jan 2th, 2024

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In The Rest Of The Paper, We Are Going To Use The Term "object" In Place Of "simple" Object For Better Readability. ... The Pixels On A Closed Contour Corresponding To The Boundary Of A Hole Will Have Border Ownership Pointing Outside Of It. See figure 1b For An Example Of A Hole. Feb 4th, 2024

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