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Features Better Immunity To SEU Than DICE In Some Technologies As It Is Less Prone To Charge Sharing [12]. However, It Is Admitted That This Design, By Its Nature, Is Still Sensitive To Single-node Upsets During The Hold State. As Illustrated In Fig. 1(b), After The Internal Node B Holding Logic 0 Is Hit, The Logic States Of This Latch Structure Would Flip. Therefore, A Stacked Quatro Was ... Apr 5th, 2024

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