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Chip Inductors (Chip Coils) - Murata Manufacturing Series Size Code In Inch (in Mm)

Structure Min. Max. Min. Inductance Range Rated Current Max. DFE18SAN_E0

DFE18SAN_G0 DFE18SBN_E0 DFE201208S DFE201210S DFE201210U DFE201610C

DFE201610E DFE201610P DFE201610R DFE201612C DFE201612E DFE201612P

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Around The World. Q Which SunTrust Card Products Will Have The Chip Card

Technology? A SunTrust Card Products In Scope Include Commercial Credit

(Corporate, Purchasing, And Executive And One Card), Small Business And

Consumer Credit, And Business Jan 5th, 2024 9 Chip Bonding At The First Level - The

Chip Collection Of Failure For An IC. 26% Of All IC Failures Are Related To The

Wirebond. Figure 9-3 Shows The Fail-ure Mechanism Breakdown For Packaged Die.

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9-3 Source: ICE, "Roadmaps Of Packaging Technology" 22510 Wirebond TAB Flip Jan

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